

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: :
:
Averett, Guy E. et al. :
:
Serial No.: 10/072,145 : Examining Group: 2811
Filed: 02/07/2002 : Examiner: Ori Nadiv

For: **SEMICONDUCTOR DEVICE AND METHOD OF PROVIDING REGIONS OF LOW SUBSTRATE CAPACITANCE**

RESPONSE TO NOTICE OF NON-COMPLIANT AMENDMENT

Honorable Commissioner of Patents,
P.O. BOX 1450
Alexandria, VA 22313-1450

SIR:

In response to the Notice of Non-Compliant Amendment mailed December 19, 2006, please reconsider the above-identified application in view of the corrected section presented hereinafter.